

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	ATTY DOCKET NO. BU9-98-050LIV1	SERIAL NO. Not yet assigned
	Moore et al.	
	FILING Concurrently herewith	GROUP Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SG	3,700,873	10/24/72	Yhap			
SG	3,918,030	11/04/75	Walker			
SG	4,145,736	03/20/79	Yamada et al.			
SG	4,251,862	02/17/81	Murayama			
SG	4,594,661	06/10/86	Moore et al.			
SG	4,660,171	04/21/87	Moore et al.			
SG	4,685,080	08/04/87	Rhodes, Jr. et al.			
SG	4,763,246	08/09/88	Holt et al.			
SG	5,084,824	01/28/92	Lam et al.			
SG	5,093,775	03/03/92	Grundmann et al.			
SG	5,185,868	02/09/93	Tran			

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FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER <i>Shae Lowe</i>	DATE CONSIDERED <i>4/6/04</i>
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INFORMATION DISCLOSURE CITATION

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ATTY DOCKET NO.
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SG	5,249,273	09/28/93	Yoshitake et al.			
SG	5,276,824	01/04/94	Shruhak et al.			
SG	5,299,321	03/29/94	Iizuka			
SG	5,371,864	12/06/94	Chuang			
SG	5,412,785	05/02/95	Skruhak et al.			
SG	5,426,591	06/20/95	Ginetti et al.			
SG	5,455,938	10/03/95	Ahmed			
SG	5,465,216	11/07/95	Rotem et al.			
SG	5,475,824	12/12/95	Grochowski et al.			
SG	5,491,640	02/13/96	Sharma et al.			
SG	5,515,302	05/07/96	Horr et al.			

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						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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INFORMATION DISCLOSURE CITATION

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ATTY DOCKET NO.
BU9-98-0501V1

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Moore et al.

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SG	5,517,432	05/14/96	Chandra et al.			
SG	5,519,627	05/21/96	Mahmood et al.			
SG	5,568,442	10/22/96	Kowalczyk et al.			
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SG	5,581,742	12/03/96	Lin et al.			
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SG	5,650,938	07/22/97	Bootehsaz et al.			
SG	5,680,332	10/21/97	Raimi et al.			
SG	5,732,255	03/1998	Verbauwhede	712	248	

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SG		Su et al., "Low Power Architecture Design and Compilation Techniques for High-Performance Processors", IEEE, 1994, pp. 489-498.
SG		Su et al., "Saving Power in the control Path of Embedded Processors", IEEE Design & Test of Computers, 1994, pp. 24-30.

EXAMINER
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